



Equivalent of Form PTO/SB/08A (09-06)

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SIXTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)				Complete if Known	
				Application Number	10/060,086
Sheet 1 of 1				Filing Date	January 31, 2002
				First Named Inventor	Sanjiv GARG
				Art Unit	2154
				Examiner Name	Larry D. Donaghue
				Attorney Docket Number	2047.0600007

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If Known)			
	US1	5,617,554	04-01-1997	Alpert <i>et al.</i>	
	US2	5,745,375	04-28-1998	Reinhardt <i>et al.</i>	
	US3	5,802,605	09-01-1998	Alpert <i>et al.</i>	
	US4	5,819,101	10-06-1998	Peleg <i>et al.</i>	
	US5	5,881,275	03-09-1999	Peleg <i>et al.</i>	
	US6	6,385,634 B1	05-07-2002	Peleg <i>et al.</i>	
	US7	6,418,529 B1	07-09-2002	Roussel	
	US8				
	US9				
	US10				
	US11				
	US12				
	US13				
	US14				
	US15				
	US16				
	US17				
	US18				
	US19				
	US20				

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
	FP1					
	FP2					
	FP3					
	FP4					
	FP5					
	FP6					
	FP7					
	FP8					
	FP9					

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Sheet	1	of	2	Attorney Docket Number	2047.0600007

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
	NPL1	<i>Intel Corporation's Responses to Transmeta's First Set of Interrogatories to Intel Corporation (Nos. 1-24) - Redacted Public Version</i> , 132 pages, dated May 18, 2007, in <i>Transmeta Corporation v. Intel Corporation</i> , in the U.S. District for the District of Delaware, Case No. 1:06-cv-00633.	
	NPL2	<i>Intel Corporation's Supplemental Responses to Transmeta Interrogatory Nos. 8, 9, 12, 19, and 21-23 - Redacted Public Version</i> , 27 pages, dated August 23, 2007, in <i>Transmeta Corporation v. Intel Corporation</i> , in the U.S. District for the District of Delaware, Case No. 1:06-cv-00633.	
	NPL3	<i>Intel Corporation's Opening Claim Construction Brief</i> , 76 pages, dated October 19, 2007, in <i>Transmeta Corporation v. Intel Corporation</i> , in the U.S. District for the District of Delaware, Case No. 1:06-cv-00633.	
	NPL4	<i>Transmeta Corporation's Opening Claim Construction Brief</i> , 66 pages, dated October 19, 2007, in <i>Transmeta Corporation v. Intel Corporation</i> , in the U.S. District for the District of Delaware, Case No. 1:06-cv-00633.	
	NPL5	Albert, AJ, "User Configurable Local Storage Registers," <i>IBM Technical Disclosure Bulletin</i> , pp. 3971-3975 (19176).	
	NPL6	Colwell, R. et al., "Architecture and implementation of a VLIW supercomputer," <i>Proc. Of the 1990 Conf. on Supercomputing</i> , (1990).	
	NPL7	Fairchild Semiconductor "Clipper 32-Bit Microprocessor – User's Manual," Prentice Hall, Inc., 531 pages (1987).	
	NPL8	Huang, J.W. et al., "Dynamically Reconfigurable Vectors," <i>IBM Technical Disclosure Bulletin</i> , pp. 3480-3483 (Dec. 1980).	
	NPL9	Adams et al., "Utilizing low level parallelism in general purpose code: the HARP project," <i>Microprocessors and Microsystems</i> , Vol. 29, No. 3, Oct. 1990.	
	NPL10	Maejima et al., "A 16-bit Microprocessor with Multi-Register Band Architecture," <i>Proc. Fall Joint Computer Conference</i> , Nov. 2-6, 1986, pp. 1014-1019.	

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Substitute for form 1449/PTO SIXTH SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete if Known	
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Sheet	2	of	2	Attorney Docket Number	2047.0600007
NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published			T ²
	NPL11	Maly <i>et al.</i> , <i>Memory Chip for 24-Port Global Register File</i> , Custom Integrated Circuits Conference, Proceedings of the IEEE, published May 12, 1991.			
	NPL12	Order Granting Request for Ex Parte Reexamination, mailed June 12, 2007 in Control No. 90/008,585, 19 pages.			
	NPL13	Order Granting Request for Ex Parte Reexamination, mailed June 16, 2007 in Control No. 90/008,644, 35 pages.			
	NPL14	Order Granting Request for Ex Parte Reexamination, mailed May 11, 2007 in Control No. 90/008,569, 27 pages.			
	NPL15				
	NPL16				
	NPL17				
	NPL18				
	NPL19				
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